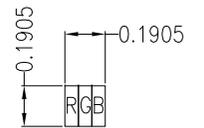
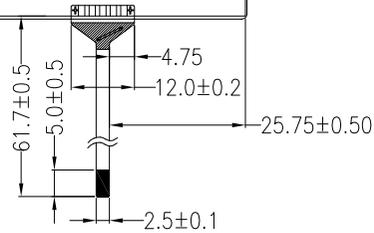
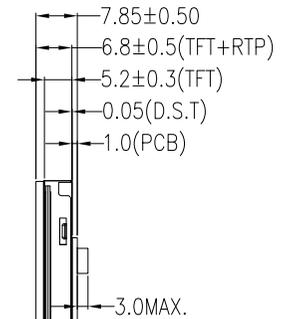
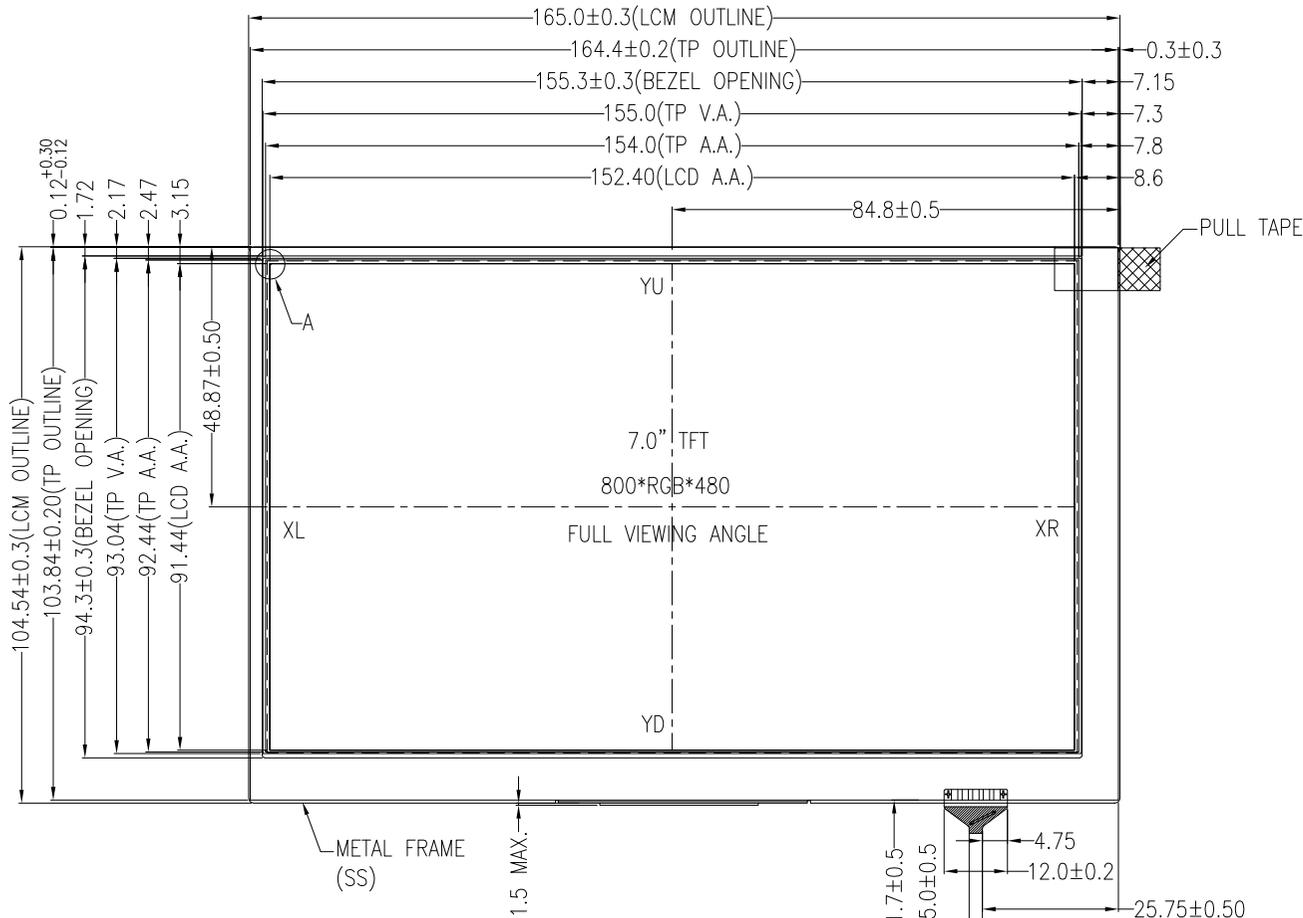


FOR ANY TOUCH PANEL OR OTHER ACCESSORIES ASSEMBLED WITH FRAME GLUE, A PERMANENT AND RELIABLE MECHANICAL STRUCTURE NEEDS TO BE DESIGNED ON CUSTOMER'S WHOLE MACHINE TO HOLD THEM IN PLACE TO AVOID LOOSE.



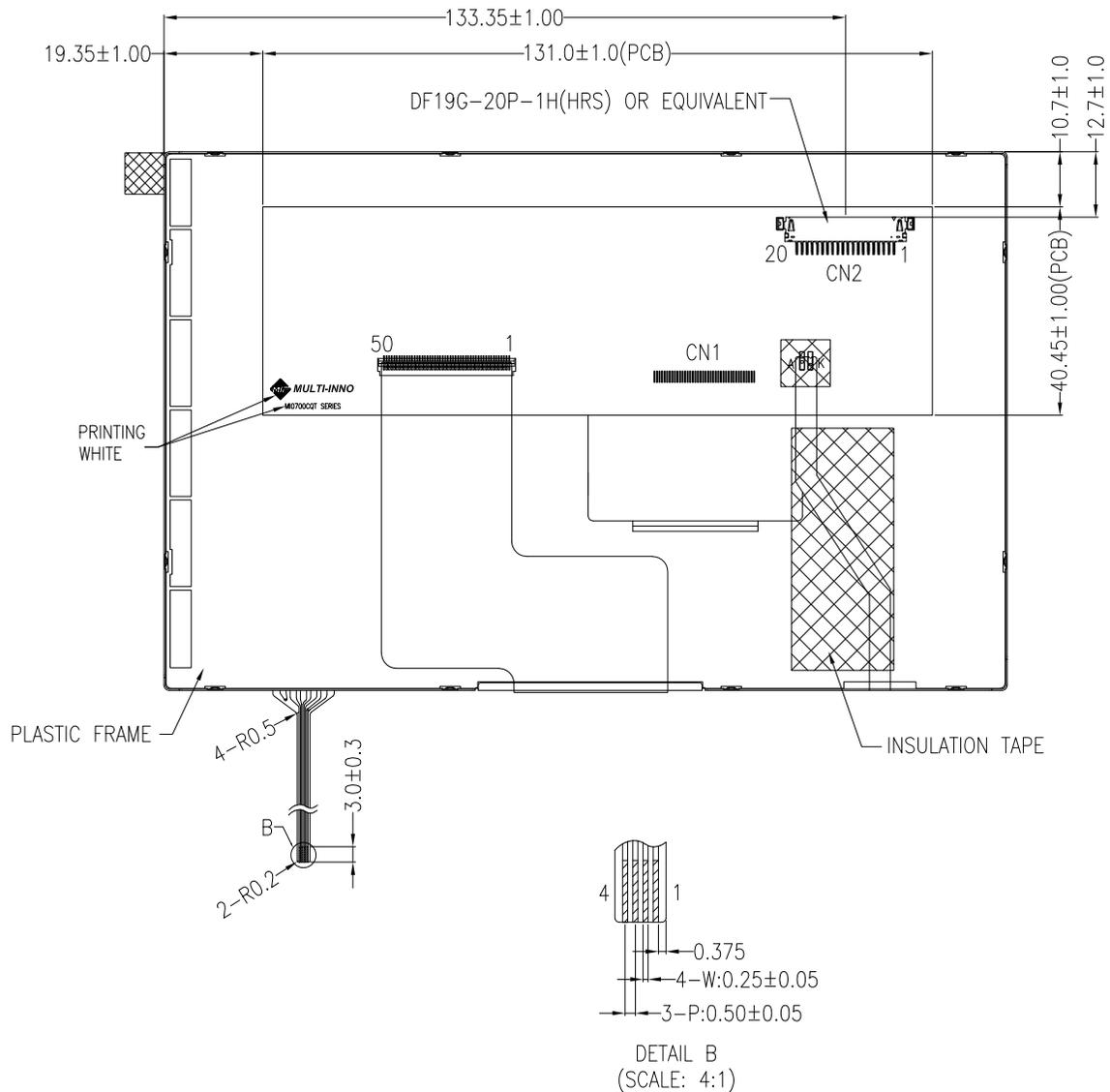
DETAIL A  
(SCALE 40:1)

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: HX8664B + HX8264-D
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. BACKLIGHT TYPE: WHITE LED  
BUILT-IN BACKLIGHT DRIVER: VBL=5.0V
8. SURFACE LUMINANCE: 480CD/M<sup>2</sup> (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. CN1 PADS RESERVED FOR RGB INTERFACE  
CN2 FOR LVDS INTERFACE
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. GENERAL TOLERANCE: ±0.3MM
13. ROHS COMPLIANT

		MULTI-INNO TECHNOLOGY CO., LTD.	
		DRAWN BY: RITA	MODULE P/N: MI0700CQT-3RP1
CHECKED BY:	SCALE: 1/1	APPROVED BY:	DESCRIPTION: TFT + RTP MODULE
VER. 01	FIRST ISSUE	RITA	2025.04.30
VER.	REVISED DESCRIPTION	REVISER	DATE
SHEET NO.: 1/2		UNIT: mm	

01	FIRST ISSUE	RITA	2025.04.30	APPROVED BY:	UNIT: mm	DESCRIPTION: TFT + RTP MODULE
VER.	REVISED DESCRIPTION	REVISER	DATE	VERSION NO: 01	SHEET NO.: 1/2	



PIN ASSIGNMENT FOR LCM	
PIN	SYMBOL
1	VCC
2	VCC
3	VSS
4	VSS
5	IN0-
6	IN0+
7	VSS
8	IN1-
9	IN1+
10	VSS
11	IN2-
12	IN2+
13	VSS
14	CLK-
15	CLK+
16	VSS
17	BL_ADJ
18	VBL
19	VSS
20	VSS

PIN ASSIGNMENT FOR RTP	
PIN	SYMBOL
1	YU
2	XL
3	YD
4	XR

**MIT** MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA		MODULE P/N:
CHECKED BY:		SCALE : 1/1	MI0700CQT-3RP1
APPROVED BY:		UNIT: mm	DESCRIPTION:
VERSION NO:	01	SHEET NO.: 2/2	TFT + RTP MODULE

01	FIRST ISSUE	RITA	2025.04.30
VER.	REVISED DESCRIPTION	REVISER	DATE